



PATENT

Examiner: Phallaka Kik

Group Art Unit: 2825

Docket: 884.141US1

TES PATENT AND TRADEMARK OFFICE

Applicant:

Kiran Ganesh et al.

Serial No.:

09/431,477

Filed:

November 1, 1999

Title:

2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS F

VLSI DESIGN

PETITION FOR A THREE-MONTH EXTENSION OF TIME

Commissioner for Patents Washington, D.C. 20231

In accordance with the provisions of 37 C.F.R. § 1.136(a), it is respectfully requested that a three-month extension of time be granted in which to respond to the Office Action mailed September 25, 2001, said period of response being extended from December 25, 2001 to March 25, 2002.

Our check in the amount of \$920.00 is enclosed to cover the required extension fee. Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

KIRAN GANESH ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9587

Reg. 20. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on **25** day of <u>March</u>, 2002.

Jane E. Brockschink

Name

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